

**Brauchli Netsolution Berg**

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**Trainings- Information for****Motorola 568xx****Metrowerks V5.x****Motorola SDK 3.x****DATES AND LOCATION**

<i>Dates</i>	<i>Location</i>	<i>Booking state</i>
14.Feb.- 18.Feb. 2004	Berg/ Switzerland	Booked out
18.April –22.April 2004	Berg/ Switzerland	Still 6 available seats/ Dev. Station
9.May- 13.May.2004	Charlotte, North Carolina/ USA	Still 6 available seats/ Dev. Station
13.June- 17.June 2004	Charlotte, North Carolina/ USA	Still 6 available seats/ Dev. Station

**AGENDA**

## Day 1

- Introducing Motorola/ Codewarrier IDE
- Introducing SDK (config.h, appconfig.h, linker.cmd)
- Introducing Startup of DSP: archstart()
- PC Master- Tool
- Debugging of Project
- Flash\_over\_jtag- tool
- Lab

## Day 2

- Architecture in the Detail of 807 Processor
- Memory Layout, Register Set, Interrupt- Handling
- Limitation of SDK (Speed Interrupt handling)
- Fractional/Integer Math with SDK Math
- SDK: Serial Interface SCI
- Lab

## Day 3

- PWM-Timer-ADC-Handling
- SDK: GPIO
- SDK: Timer
- Lab with SDK Timer Callback vs. SDK-ISR vs. Assembly ISR
- Lab

## Day 4

- SDK: ADC and Implementation of ADC on lower level
- SDK: Encoder Interface
- Lab

## Day 5

- SDK: CAN

- SDK: FLASH
- Recommended Software Design for new Project
- Lab

Training courses are limited to 5-6 persons

## FURTHER INFORMATION

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